S/N 09/136,680

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Christophe J. Chevallier

Examiner: Luong Nguyen

Serial No.:

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Title:

CMOS IMAGER WITH INTEGRATED NON-VOLATILE MEMORY

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MAY 2 8 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Technology Center 2600

This paper responds to the Office Action mailed on <u>January 20, 2004</u>. This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one month extension of the period for responding to the Office Action, thereby moving the deadline for response from April 20, 2004 to May 20, 2004.

